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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Sadeg M. Faris

SERIAL NO.: 10/719,666

FILING DATE: November 20, 2003

FOR: Method And System For Increasing Yield  
Of Vertically Integrated Devices

)  
) Group Art Unit  
) 2891  
)  
) Examiner  
) Chaudhari, Chandra

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**TRANSMITTAL OF FORMAL DRAWINGS**

Sir:

In response to the Notice of April 4, 2006 hereof, applicants respectfully submit  
50 replacement sheets of drawings for the above referenced application.

By:

Ralph J. Crispino  
Registration No. 46,144

April 19, 2006  
REVEO, INC.  
Customer No. 26665  
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I hereby certify that this correspondence is being deposited  
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Date